	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	5	connex adj2 engine	US-PGPUB; USPAT	2006/04/20 22:39
2	BRS	L2	5	connex adj2 memory	US-PGPUB; USPAT	2006/04/20 22:39
3	BRS	L3	0	2 not 1	US-PGPUB; USPAT	2006/04/20 22:39
4	BRS	L4	35	cartesian adj2 controller	US-PGPUB; USPAT	2006/04/20 22:59
5	BRS	L5	71420	issu$3 near3 (instruction command)	US-PGPUB; USPAT	2006/04/20 22:59
6	BRS	L6	4	4 and 5	US-PGPUB; USPAT	2006/04/20 23:05
7	BRS	L7	635	memory adj engine	US-PGPUB; USPAT	2006/04/20 23:06
8	BRS	L8	373	7 and controller	US-PGPUB; USPAT	2006/04/20 23:06
9	BRS	L9	68	5 and 8	US-PGPUB; USPAT	2006/04/20 23:06
10	BRS	L10	35	9 and parallel	US-PGPUB; USPAT	2006/04/20 23:07
11	BRS	L11	30	10 not 1	US-PGPUB; USPAT	2006/04/20 23:45
12	BRS	L12	45933	clock adj cycle	US-PGPUB; USPAT	2006/04/20 23:45
13	BRS	L13	13	11 and 12	US-PGPUB; USPAT	2006/04/20 23:45